

Sapphire, SiC, AlN, Si and diamond-substrate material for GaN HEMT and LED

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Performance of AlGaIn/GaN High Electron Mobility Transistor on Sapphire, Si, AlN, SiC and Diamond substrates are studied. AlGaIn/GaN HEMT on Sapphire shows negative differential conductivity region, at large drain bias, due to large accumulation of heat in channel at gate drains edge. Remarkable improvement in characteristics is observed for HEMT devices on high thermal conducting SiC and Diamond substrate. No significant improvements in characteristics are observed for HEMT structure on Si and AlN substrate. Effect of rise in junction temperature of GaN LED on Sapphire is analyzed. Results show that, there is accumulation of heat in area between two electrodes.

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1. Introduction

III-N material based devices have gained more attention, due to their good thermal conductivity, high breakdown field (GaN $\sim 3 \times 10^6$ V/cm) and high saturation velocities (GaN $\sim 3 \times 10^7$ cm/s) [1]. These devices can operate at high temperature, without degradation in performance. Reliability, performance and life time combination of these devices are much higher than any other material based devices. The Polarization field in III-V materials are larger than others III-V compound semiconductor. GaN based devices are showing great performance in field of power and optoelectronic. The performance level of AlGaIn/GaN HEMTs devices are increasing rapidly from last few years. Output power densities of 30W/mm [2] for AlGaIn/GaN HEMT device on SiC were achieved and AlGaIn/GaN amplifier with 1kW at 3.2GHz was reported [3]. At present, GaN based LEDs are used for a variety of applications, including back lighting of liquid crystal displays, traffic signals, full color displays, and white LEDs. High efficiency white LEDs have potential to replace fluorescent lamps.

GaN based devices are facing many problems due to non-availability of bulk GaN material. So there structures are hetero-epitaxially grown on foreign substrate. GaN devices are general grown on SiC [4, 5], Si [6, 7, 8] and Sapphire [9, 10, 11, 12] by MOCVD or MBE. SiC is most suitable material for these devices, with lattice mismatch of 3 %. But the main drawback of SiC is its high cost. Composite SiC substrate are showing promise candidate for GaN devices. SiC on poly crystal SiC shows same level of performance as of single crystal SiC substrate [13, 14], and can be a promising candidate for GaN HEMT. Recently researchers have been attracted to diamond

substrate [15] due to its large thermal conductivity (10 to 30 W/(cm-K)). Dislocation density in GaN buffer grown on Sapphire are order of 10^{9-10} cm⁻² [16, 17], and GaN buffer grow on SiC shows 10^8 dislocations per cm² [18]. These dislocation centers acts as non radiative centers and degrades the output performance of LED. Introduction of low temperature nucleation layer [19], annealing [20], Fe [21], delta doping [22, 23] greatly improved the quality of GaN epitaxial layer. High quality epitaxial layer with low dislocation density and semi insulating buffer, are primary need, for GaN based power and optoelectronic devices.

Self heating at large bias is commonly observed in high power devices. Generated heat must be conducted out from the active region to maintain performance of device. Thus thermal conductivity of the substrates plays a significant role in the device performance and reliability. In this work, different substrate materials for AlGaIn/GaN HEMT are studied at high drain bias, by ATLAS, a commercially available software package from Silvaco. It is very important to determine the junction temperature of the LED during operation. Performance of AlGaIn/InGaIn/GaN LED structure on Sapphire and SiC are studied by ATLAS.

2. Simulation approach

HEMT and LED structure used in this study are shown in Fig. 1 and Fig. 2 respectively. Parameters used in simulation are listed in Table 1. For AlGaIn/GaN HEMT gate length is 0.7 μ m. Gate to source and gate to drain spacing are 1 and 1.1 μ m respectively. Aluminum composition (x) in AlGaIn/GaN HEMT is 0.30. For LED

Aluminum and Indium composition in AlGa_N and InGa_N are 0.20.

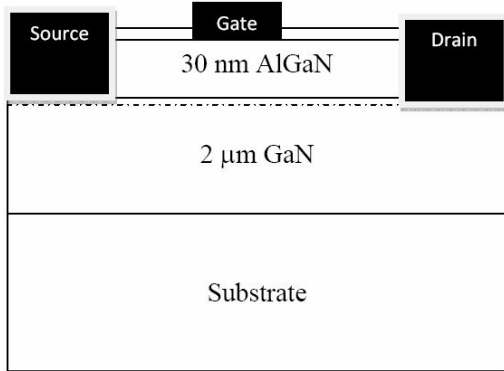


Fig.1 Structure of AlGa_N/Ga_N HEMT.

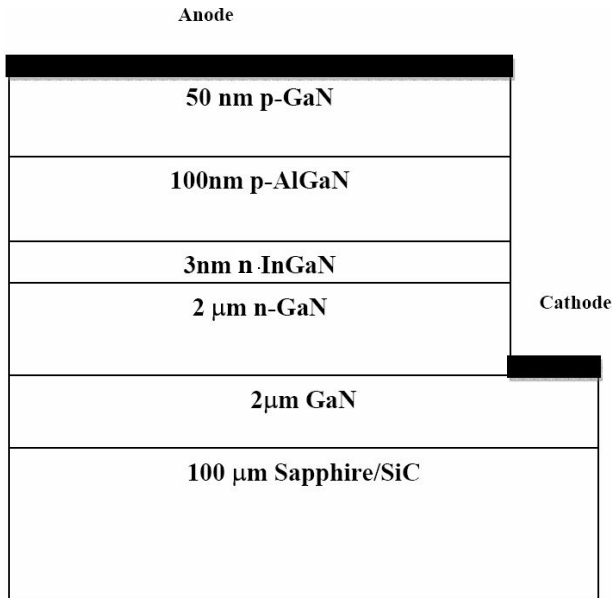


Fig. 2. Structure of AlGa_N/InGa_N/Ga_N LED.

Temperature dependent thermal conductivity model is used for AlGa_N/Ga_N HEMT and LED and its form is

$$k(T) = (a + bT + cT^2)^{-1} \quad (1)$$

where a, b and c are thermal constants.

Band gap of III-N semiconductors at temperature T is given by.

$$E_g(T) = E_g(0) - m \cdot T^2 / (T+n) \quad (2)$$

where E_g(T) is Bandgap of semiconductor at temperature T, E_g(0) is bandgap at 0K and m, n are thermal constants.

For ternary compounds

$$E_{g(AxBy(1-x)N)} = E_{g(AN)}x + E_{g(BN)}(1-x) - q \cdot x(1-x) \quad (3)$$

where x is mole fraction of ternary semiconductor and q is bowing parameter.

For Ga_N m = 0.909 x 10⁻³, n = 830

And for AlGa_N bowing parameter (q) is 1.3

Spontaneous and piezoelectric induced charges are the major source of carriers in Ga_N based hetero-junction devices [24]. Polarization charges at interface are given by

$$P_{int} = P_{total}(\text{Top layer}) - P_{total}(\text{bottom layer}) \quad (5)$$

and

$$P_{total} = \text{Spontaneous polarization} + \text{Piezoelectric Polarization} \quad (6)$$

Spontaneous polarization of Ga_N is -0.034 C/m² and for AlGa_N layer is given by [25]

$$P_{sp} = -0.09x - 0.034(1-x) + 0.021x_n(1-x) \quad (7)$$

And piezoelectric polarization in strained AlGa_N grown on Ga_N is given by

$$P_{pz} = -0.0525x + 0.0282 \times (1-x) \quad (8)$$

Thin Al_{0.3}Ga_{0.7}N epitaxial layer grown on Ga_N experience biaxial tensile stress and net polarization charge at interface is given by

$$P_{int}(\text{AlGa}_N/\text{Ga}_N) = 1.38 \times 10^{-13} \text{ cm}^{-2}$$

3. Results and discussion

I-V characteristics curve of AlGa_N/Ga_N HEMT on Sapphire are shown in Fig. 3. Maximum channel current of 857mA/mm is obtained for HEMT on Sapphire. Maximum transconductance of 201mS/mm is obtained for HEMT on Sapphire.

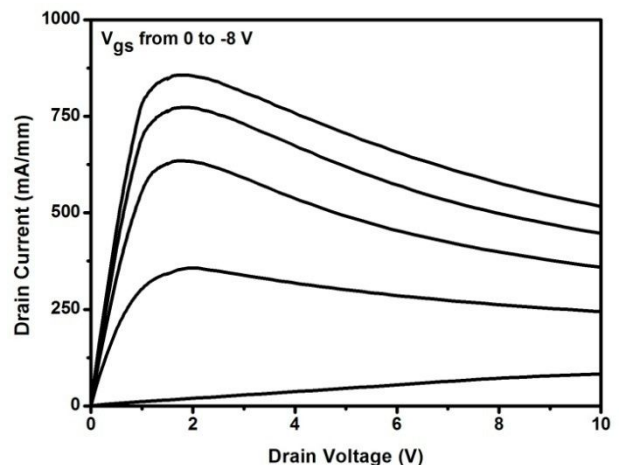


Fig. 3. I-V Curves of AlGa_N/Ga_N on sapphire.

Thermal analysis of AlGaIn/GaN HEMT on sapphire shows large accumulation of heat in channel near gate edge of drain side Fig. 4. Channel temperature rises to 683K for $V_{ds} > 15V$. The large accumulation of heat in channel is due to poor thermal conductivity of Sapphire. For HEMT on SiC, a remarkable improvement in device characteristics is obtained. Maximum channel current of 993mA/mm is obtained for AlGaIn/GaN HEMT on SiC.

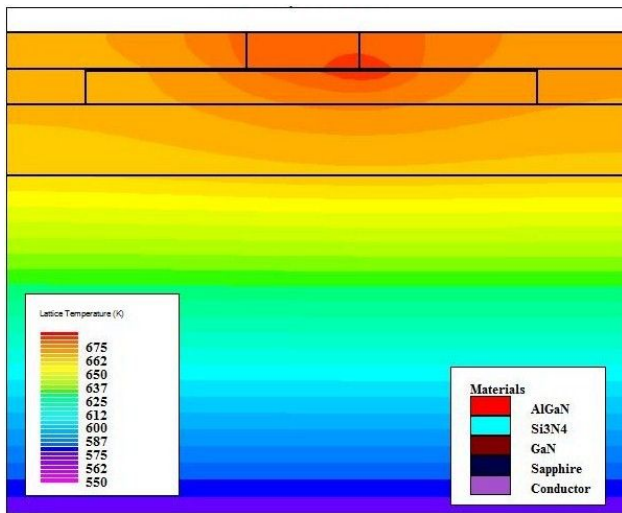


Fig. 4. AlGaIn/GaN HEMT on sapphire.

Further improvement in channel current is observed for AlGaIn/GaN HEMT on Diamond Fig. 5. Maximum channel current of 1566mA/mm is obtained for HEMT on Diamond.

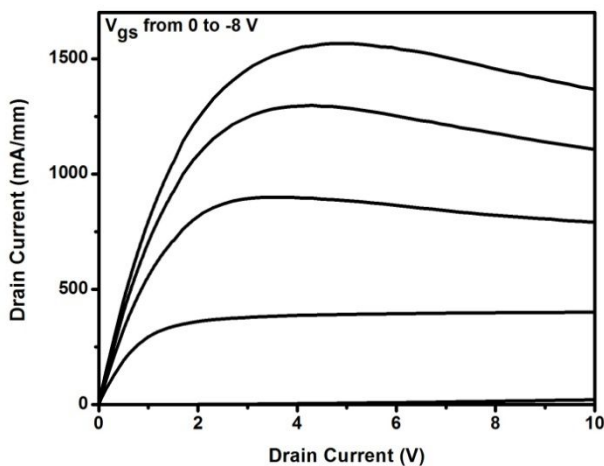


Fig. 5. I-V Curves of AlGaIn/GaN on diamond.

AlGaIn/GaN HEMT on AlN and Si does not show any improvement Fig. 6. Maximum transconductance of 273mS/mm and 213 mS/mm is obtained for HEMT on Diamond and SiC respectively. In pinch off condition, drain current falls to zero for HEMT on Diamond, but for HEMT on Sapphire channel current does not fall to zero even after $V_{gs} = -10V$ Fig. 7. This result shows that

thermal noise is generated in HEMT, due to device heating. Transfer curve for AlGaIn/GaN on different substrates are shown Fig. 8.

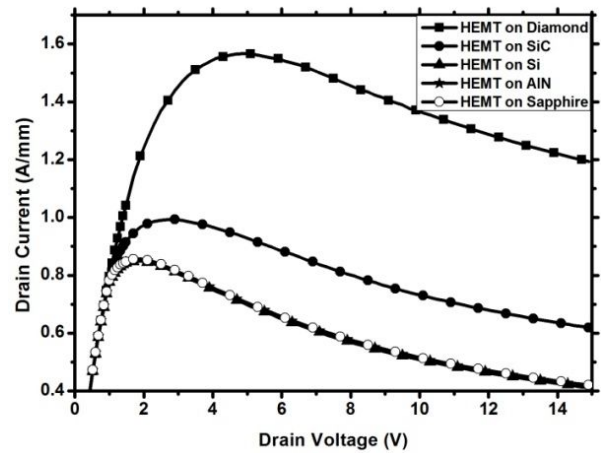


Fig. 6. I-V Curves of AlGaIn/GaN HEMT on different substrates at $V_{gs}=0V$.

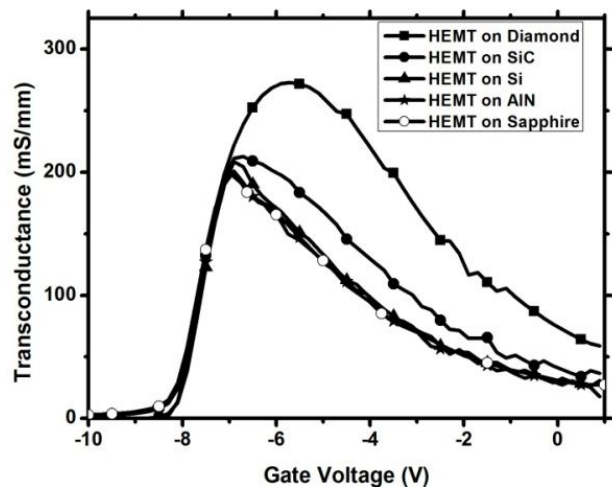


Fig. 7. Transconductance for AlGaIn/GaN HEMT on different substrates.

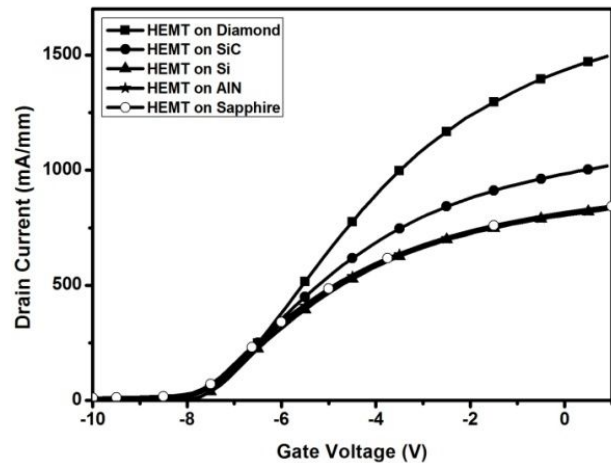


Fig. 8. Transfer curve for AlGaIn/GaN HEMT on different substrates.

AlGaN/InGaN/GaN LED structure on Sapphire and SiC substrate (Fig. 2) are studied by ATLAS. Device parameters used in this study are given in Table 1. These improvements in device characteristics on SiC and Diamond can directly correlate with channel temperature as shown in Fig. 9.

Table 1. HEMT and LED materials parameter used in this study.

Parameters	Value
Interface charge	$1.38 \times 10^{13} / \text{cm}^2$
Thermal contact	300 K
Thermal conductivity of diamond	20 W/(cm-K)
$\Delta E_c / \Delta E_v$ (Band Offset)	0.7/0.3
Thermal resistance at substrate	200 W/(cm ² -K)
GaN Electron mobility	300 cm ² /(V-s)
Schottky barrier height of gate metal	1.23 eV
Electron saturation velocity (AlGaN)	1.12×10^7 cm/s
Electron saturation velocity (GaN)	1.91×10^7 cm/s
Band Gap Al _{0.3} Ga _{0.7} N	3.97 eV
LED area	350 * 350 μm^2

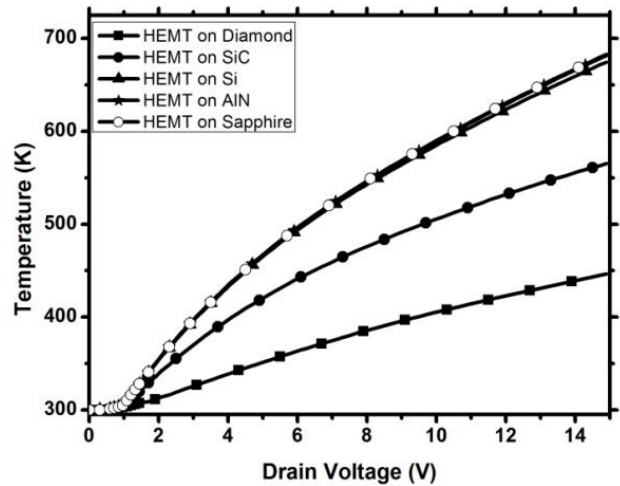


Fig. 9. Transfer curve for AlGaN/GaN HEMT on different substrates.

Fig. 10 shows the channel temperature of AlGaN/GaN HEMT on Diamond falls to 446K.

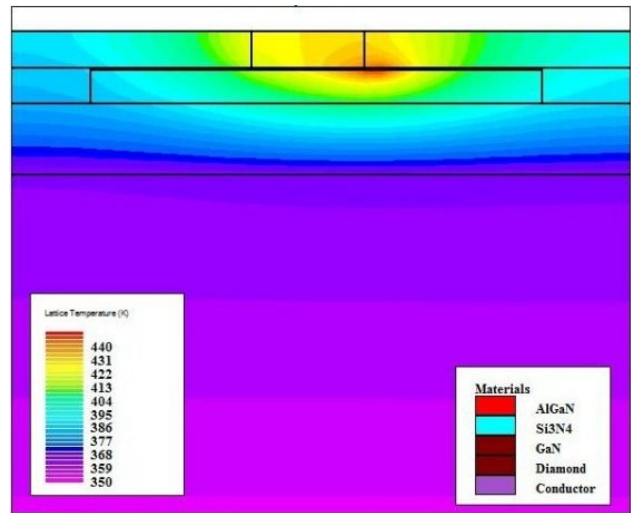


Fig. 10. AlGaN/GaN HEMT on diamond.

Results extracted from AlGaN/GaN HEMT on different substrates are shown in Table 2.

Table 2. Results extracted from AlGaN/GaN HEMT devices on different substrates.

Substrate	Max. channel current (mA/mm)	Channel temperature (K) at $V_{ds}=15\text{V}$ and $V_{gs}=0\text{V}$	Channel temperature (K) at $V_{ds}=3\text{V}$ and $V_{gs}=1\text{V}$	Maximum transconductance (mS/mm)	Threshold voltage (V)
Diamond	1566	446	325	273	-8
SiC	993	565	370	213	-8
Si	850	674	395	208	-8
AlN	855	682	397	201	-8
Sapphire	857	683	396	201	-8

Thermal analysis results shows that maximum heating is observed, in the area between two electrodes, and the

temperature rises to 330K for LED structure on Sapphire biased at 10V Fig. 11.

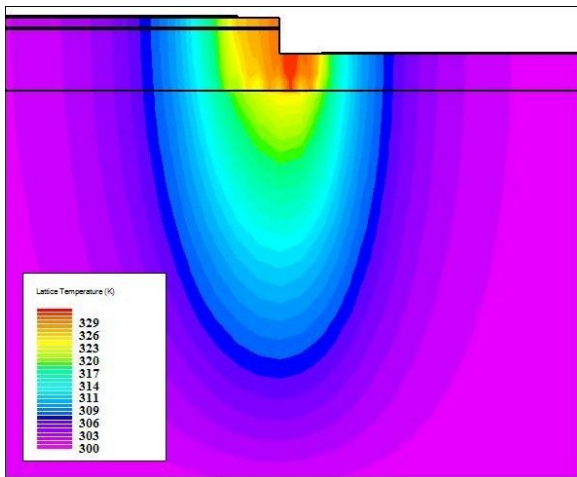


Fig. 11. AlGaIn/InGaIn/GaN LED on sapphire ($V_{anode} = 10$ V).

By changing the substrate from Sapphire to SiC the maximum temperature in this region falls to 320K. I-V curves of LED structure on Sapphire and SiC substrate are shown in Fig. 12. Very little improvement in anode current is observed for device on SiC. Electroluminescence (EL) curve of LED structure on Sapphire and SiC are shown in Fig. 13. No change in EL peak is observed.

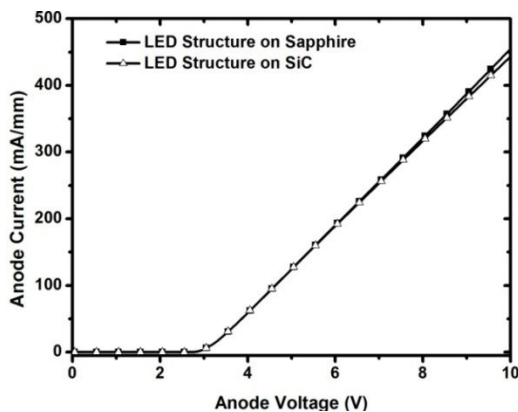


Fig. 12. I-V Curves for AlGaIn/InGaIn/GaN LED on sapphire and SiC.

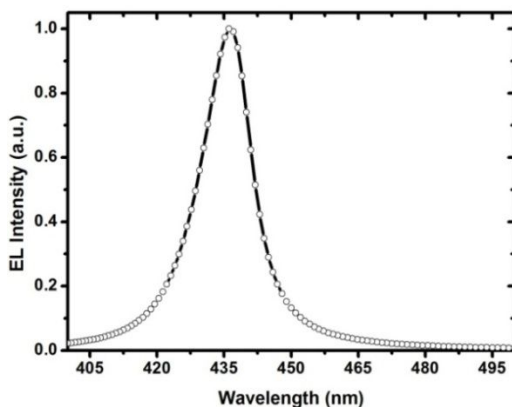


Fig. 13. EL Curve for AlGaIn/InGaIn/GaN LED on sapphire/SiC.

Variation of temperature of device with anode current is shown in Fig. 14.

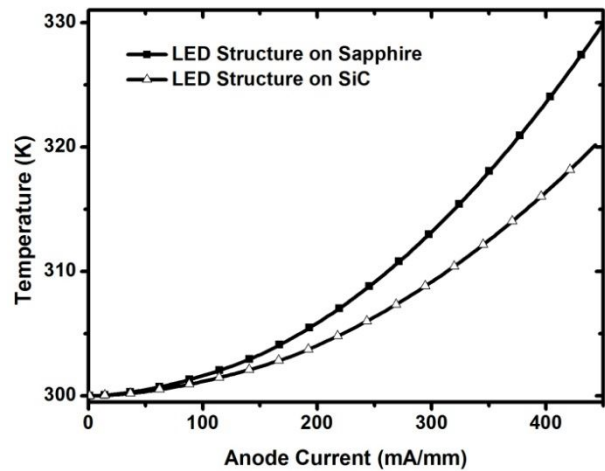


Fig. 14. Variation of LED device temperature with respect to anode voltage.

4. Conclusions

Effects of high thermal conducting substrate materials on performance of AlGaIn/GaN HEMT and AlGaIn/InGaIn/GaN LED devices are studied by ATLAS. AlGaIn/GaN HEMT on sapphire shows maximum drain current of 857mA/mm. I-V curves of AlGaIn/GaN HEMT on sapphire shows large negative differential region at higher drain bias. Thermal analysis of the device shows, a large accumulation of heat in channel at gate edge. Great improvement in channel current is observed for device on high thermal conductive substrate. Maximum channel current of 0.993A/mm and 1.57A/mm is observed for HEMT on SiC and Diamond respectively. Maximum transconductance of 201mS/mm, 213mS/mm and 273mS/mm are observed for HEMT on Sapphire, SiC and Diamond respectively. No improvement in output characteristics are observed for device on Si and AlN substrate. Thermal analysis of AlGaIn/InGaIn/GaN LED on Sapphire shows that, large accumulation of heat in area between two electrodes. Very little improvement in performance is obtained for AlGaIn/InGaIn/GaN LED on SiC substrate. In conclusion the heat generated inside the device, have very impact on device performance and this heat must be extracted out to maintain the performance of device. High thermal conductive substrate material have great impact in AlGaIn/GaN HEMT and must be employed in device manufacturing to achieve maximum performance from the device.

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